PATENT NUMBER and ISSUE DATE

## U.S. UTILITY Patent Application

APPL NUM 10053198	FILING DATE 01/17/2002	3 <u>2</u> 4	SUBCLASS	2862	EXAMINER:
**APPLICANT	S: Senthilk	kumar Chi	nnugounder; L	_ee Tea; F	Fulton Robert; Volk-Andrew;
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	ons met rledged Examiners's inti				ATTORNEY DOCKET NO 0559/649001/P12971
TLE: Low pow	ver oscillator circuit				U.S.DEPT OF COMM/PAT&TM-PTO-435L(Rev. 12-

NOTICE OF ALLOWANCE MAILED			CLAIMS ALLOWED					
		Assistant Examiner	Total Claims		Print Claim for O.G			
· ISSUE FEE			DRAWING					
Amount Due	Date Paid	1	Sheets Drwg.	Figs.Drwg.	Print Fig.			
		Primary Examiner						
TERMINAL.		PREPARED FOR ISSUE	Application Examiner					
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